

AMENDMENT IN THE CLAIMS

Please amend Claim 24.

Please cancel Claims 29-30 and 34-35.

1.-22. (canceled).

23. (previously presented) A module for determining a driving signal timing for a liquid crystal display (LCD) device, comprising:

a flexible base substrate;

a gate-driving signal input line formed on one side of the base substrate that applies a gate-driving signal to the gate-driving signal input line;

a gate-driving IC mounted on the flexible base substrate to be connected to the gate-driving signal input line; and

a plurality of gate-driving signal output lines formed on the flexible base substrate that are connected to output terminals of the gate driving IC.

24. (currently amended) The module of claim 23, wherein a difference between a level of the gate-driving signal from [[the]] a signal transmitting line and a level of the gate-driving signal from the last output terminal of the output terminals of the gate-driving IC is adjusted to be within a predetermined range.

25. (previously presented) The module of claim 24, wherein a portion of the signal transmitting line passes through the inside of the gate-driving IC while the level of the gate-driving signal from the signal transmitting line and the level of the gate-driving signal from the last output terminal of the output terminals of the gate-driving IC are within a predetermined range.

26. (previously presented) The module of claim 24, wherein the level of the gate-driving signal from the signal transmitting line and the level of the gate-driving signal from the last output terminal of the output terminals of the gate-driving IC are within a predetermined range by adjusting an intrinsic resistance of the signal transmitting line.

27. (previously presented) The module of claim 23, wherein the flexible base substrate has a signal input line connected to the gate-driving IC and at least one signal transmitting line separated from the signal input line.

28. (previously presented) The module of claim 27, wherein the level of the gate-driving signal from the signal transmitting line and the level of the gate-driving signal from the last output terminal of the output terminals of the gate-driving IC are within a predetermined range.

29.-30. (canceled)

31. (previously presented) The module of claim 23, wherein the gate driving IC linearly modifies a level of the gate-driving signal inputted to a first gate line through a last gate line of a plurality of gate lines formed on a TFT substrate when diverging the applied gate-driving signal in a parallel way so as to input the applied gate-driving signal to the plurality of gate lines, and then, output the linearly modified gate-driving signal through output terminals thereof.

32. (previously presented) The module of claim 23, wherein the plurality of gate-driving signal output lines are adapted to allow the gate-driving signal outputted from the output terminals of the gate-driving IC to have a linear level and to be applied to the plurality of gate lines,

33. (previously presented) The module of claim 32, wherein a signal transmitting line is formed on the flexible base substrate and connected to the gate-driving signal input line in parallel, so that the gate-driving signal applied to the gate-driving signal input line is not applied to the gate-driving IC and is bypassed toward outside of the flexible base substrate.

34.-35. (canceled)